

	Document name	Document type	Exhibit / Paper	Filing date	Filing party	Availability
1	Exhibit 3003	Exhibit	3003	6/25/2024	Board	Public
2	Exhibit 3002	Exhibit	3002	4/18/2024	Board	Public
3	Exhibit 3001	Exhibit	3001	4/12/2024	Board	Public
4	Patent Owner's Oral Hearing Demonstratives	Exhibit	2036	2/18/2025	Patent Owner	Public
5	U.S. Patent No. 6,545,315 to Hshieh et al., "Trench DMOS transistor having reduced punch-through"	Exhibit	2034	9/16/2024	Patent Owner	Public
6	U.S. Patent No. 6,084,264 to Darwish, "Trench MOSFET having improved breakdown and on-resistance"	Exhibit	2033	9/16/2024	Patent Owner	Public
7	Sharma, R. S., and M. A. Peer. "Some Features of Avalanche Breakdown Phenomena Part 1: Transient Behavior"	Exhibit	2032	9/16/2024	Patent Owner	Public
8	Excerpts of Williams, Principles and Elements of Power Electronics, ISBN 978-0-9553384-0-3 (2003)	Exhibit	2031	9/16/2024	Patent Owner	Public
9	Excerpts of Sedra, Adel S., Microelectronic Circuits (5th Ed. 2004)	Exhibit	2030	9/16/2024	Patent Owner	Public
10	Definition of "cover" from <a href="https://www.merriam-webster.com/dictionary/cover">https://www.merriam-webster.com/dictionary/cover</a>	Exhibit	2029	9/16/2024	Patent Owner	Public
11	U.S. Patent No. 8,884,409 to Wong, "Wafer backside doping for thermal neutron shielding"	Exhibit	2028	9/16/2024	Patent Owner	Public
12	U.S. Patent No. 4,315,781 to Henderson, "Method of controlling MOSFET threshold voltage with substrate doping"	Exhibit	2027	9/16/2024	Patent Owner	Public
13	Suzuki, Kunihiro, A. Satoh, and T. Sugii. "Counter doping into uniformly and heavily doped channel"	Exhibit	2026	9/16/2024	Patent Owner	Public
14	Definition of "layer" from <a href="https://dictionary.cambridge.org/us/dictionary/english/layer">https://dictionary.cambridge.org/us/dictionary/english/layer</a>	Exhibit	2025	9/16/2024	Patent Owner	Public
15	U.S. Patent No. 3,025,589 to Hoerni, "Method of manufacturing semiconductor devices"	Exhibit	2024	9/16/2024	Patent Owner	Public
16	Definition of "layer" from <a href="https://www.merriam-webster.com/dictionary/layer">https://www.merriam-webster.com/dictionary/layer</a>	Exhibit	2023	9/16/2024	Patent Owner	Public
17	Definition of "contact" from <a href="https://www.lamresearch.com/technical-glossary/">https://www.lamresearch.com/technical-glossary/</a>	Exhibit	2022	9/16/2024	Patent Owner	Public
18	Definition of "contact" from <a href="https://www.merriam-webster.com/dictionary/contact">https://www.merriam-webster.com/dictionary/contact</a>	Exhibit	2021	9/16/2024	Patent Owner	Public
19	Definition of "lateral" from <a href="https://www.merriam-webster.com/dictionary/lateral">https://www.merriam-webster.com/dictionary/lateral</a>	Exhibit	2019	9/16/2024	Patent Owner	Public
20	Wang, Jingshan. High-Power Vertical GaN Electronic Devices Formed by Epitaxial Lift-Off. Diss. University of Michigan, 2018.	Exhibit	2018	9/16/2024	Patent Owner	Public
21	Search results from scholar.google.com for article search "'lateral contact layer' MOSFET"	Exhibit	2017	9/16/2024	Patent Owner	Public
22	Additional Excerpts from Semiconductor Glossary	Exhibit	2016	9/16/2024	Patent Owner	Public
23	Zhang, Ruizhe, and Yuhao Zhang. "Power device breakdown mechanism and characterization: Review"	Exhibit	2015	9/16/2024	Patent Owner	Public
24	Transcript of Aug. 22, 2024 Deposition of David Kuan-Yu Liu	Exhibit	2014	9/16/2024	Patent Owner	Public
25	Curriculum Vitae of Dean Neikirk, Ph.D.	Exhibit	2013	9/16/2024	Patent Owner	Public
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29	Corrected Neikirk Decl. – AOS v. Force MOS	Exhibit	2009	2/27/2024	Patent Owner	Public
30	U.S. Patent No. 6,888,196 (Kobayashi)	Exhibit	2008	2/27/2024	Patent Owner	Public
31	Semiconductor Glossary (excerpts)	Exhibit	2007	2/27/2024	Patent Owner	Public

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