

Contact

www.linkedin.com/in/arshan-aga-5b573a91 (LinkedIn)

Top Skills

Engineering

Embedded Systems

ASIC

Arshan Aga

Vice President of Analog Engineering at Credo Semiconductor, Inc.
Mountain View, California, United States

Experience

Credo Semiconductor, Inc.
Vice President of Analog Engineering
July 2017 - Present (8 years 5 months)
Milpitas, California

VitalConnect
Vice President of Hardware Engineering
August 2011 - July 2017 (6 years)
San Jose, California

Teranetics
Senior Design Engineer
January 2010 - August 2011 (1 year 8 months)

Project FT, Inc
Senior Design Engineer
March 2008 - January 2010 (1 year 11 months)
San Jose, CA

Marvell Semiconductor
Manager of Engineering
February 2001 - March 2008 (7 years 2 months)

Education

McGill University
Master of Engineering (M.Eng.), Electrical and Electronics
Engineering · (1998 - 2001)

McGill University
Bachelor of Engineering (B.Eng.), Electrical and Electronics
Engineering · (1994 - 1998)