



PRESS RELEASE
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Credo Introduces PAM4 DSP for High Performance Data Centers and Enterprise Networks

San Jose, Calif., March 17, 2021 – [Credo](#), a global innovation leader in high-performance, low-power serial connectivity solutions, today announced it is expanding its product offering with [Black Hawk](#) (CRT55321) that leverages the company’s unique PAM4 DSP architecture to deliver industry-leading low power and reach. The PAM4 retimer and gearbox provide guaranteed, end-to-end signal integrity in backplane, front panel and copper applications. The CRT55321 expands the bandwidth capacity of next-generation network infrastructure at Hyperscale data centers, enterprise networks, and service providers while consuming less than 50% power of existing solutions.

The CRT55321 is a 32-lane device that supports 16 lanes on the host side and 16 lanes on the line side. The extended reach performance provides robust signal integrity for PAM4 across the most difficult channels, including legacy backplanes. The device can be used in

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computing,” said Scott Feller, Vice President of Marketing at Credo. “Credo has leveraged its extensive analog and mixed-signal expertise to develop a unique DSP architecture to create the Black Hawk product which is 50% lower power than existing products. Credo continues to innovate with new product solutions to help solve the signal integrity issues of the next-generation data center interconnects.”

“Credo has established a significant market share in Hyperscale data centers with their retimer and gearbox solutions. The new Black Hawk device, which offers a 50% reduction in power consumption, enables those data centers to lower power consumption while driving even higher bandwidth,” says Alan Weckel, Founder and Technology Analyst at 650 Group. “As Cloud data centers deploy a larger number of 400G and 800G ports, low power solutions such as Credo’s Black Hawk will enable the migration.”

KEY FEATURES

- Support long-reach links defined in CEI-25G-LR, CEI-56G-LR-PAM4 and IEEE802.3cd
- Support chip-to-chip and chip-to-module interfaces defined in CEI-28G-SR/VSR, CEI-56G-SR-PAM4, and IEEE 802.3bs
- Pin-compatible with Credo’s previous generation retimer and gearbox
- Support auto-negotiation and link training
- Fully adaptive and programmable RX equalization with CTLE and DFE
- Built-in diagnostics
- IO polarity switch control

AVAILABILITY

The CRT55321 is entering production shipments in a 23x23mm BGA package. Additionally, Credo offers an evaluation kit that allows customers to quickly evaluate performance in their specific application environment.

Customers interested in learning more about Black Hawk as well as Credo’s other current

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end-to-end signal integrity for next-generation platforms requiring 25G, 50G, and 100G signal lane-rate connectivity for 100G, 200G, 400G, and 800G port enabled networks.

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