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Re: Defendant TSMC's stipulation related to *Advanced Integrated Circuit Process LLC v. Taiwan Semiconductor Manufacturing Co.*, No. 2:24-cv-00623-JRG (E.D. Tex.) and related *inter partes* review proceedings

Counsel,

Defendant Taiwan Semiconductor Manufacturing Co. Ltd. ("TSMC") hereby stipulates that:

If either:

(a) in response to any of the petitions filed by TSMC against U.S. Patent Nos.

- 8,198,686 (IPR2025-00682),
- 8,907,425 (IPR2025-00683),
- 7,579,227 (IPR2025-00828),
- 7,923,764 (IPR2025-00829)
- 8,253,180 (IPR2025-00830),
- 8,587,076 (IPR2025-00831), or



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- 8,796,779 (IPR2025-00832),

the PTAB institutes *inter partes* review and does not subsequently vacate institution,

or

(b) in response to any of United Microelectronics Corporation's ("UMC") petitions against U.S. Patent Nos.

- 8,198,686 (IPR2025-01091),
- 8,907,425 (IPR2025-01090),
- 7,579,227 (IPR2025-01076),
- 7,923,764 (IPR2025-01079)
- 8,253,180 (IPR2025-01092),
- 8,587,076 (IPR2025-01093), or
- 8,796,779 (IPR2025-01053),

the PTAB institutes *inter partes* review, which is not subsequently vacated or terminated before Final Written Decision,

then

TSMC will not pursue against any of the claims challenged in any instituted IPR proceeding identified above, in any pending litigation, the following:

- (i) the specific grounds raised in that instituted IPR proceeding;
- (ii) any other grounds that could have reasonably been raised before the PTAB in that instituted proceeding (i.e., any ground that could have reasonably been raised under pre-AIA §§ 102 or 103 on the basis of prior art patents or printed publications); or
- (iii) any other invalidity ground under pre-AIA §§ 102 or 103 involving any type of legally recognized prior art, including any patent, printed publication, or system art.

Defendant will inform the Board of this stipulation.

Sincerely,

Karrie Wheatley

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