

<https://www.nist.gov/chips/micron-new-york-clay>



## **CHIPS FOR AMERICA** (<https://www.nist.gov/chips>)

# Micron (New York)

Clay, NY 13041

**Congressional District** 22nd District

## Key Facts



## Project Summary

The U.S. Department of Commerce awarded Micron Technology Inc. (“Micron”), through its subsidiaries, Micron Idaho Semiconductor Manufacturing (TRITON) LLC and Micron New York Semiconductor Manufacturing LLC, direct funding under the CHIPS and Science Act. This funding will support Micron’s two-decade vision to invest approximately \$100 billion in New York and \$50 billion in Idaho, which will help the U.S. grow its share of advanced memory manufacturing from less than 2% today to approximately 10% by calendar year 2035. With this funding, Micron plans to expand the development and production of the most advanced memory semiconductor technology in New York and Idaho, which is expected to create approximately 23,000 direct jobs.

## Economic and National Security Impact

This investment in Micron will help strengthen U.S. economic and national security by bolstering a reliable domestic supply of the leading-edge DRAM chips that are important components for advanced technologies such as wireless communications, personal computing, high-performance compute, automotive, and artificial intelligence. Micron's technology will also enable the highest performance memory, known as High-Bandwidth Memory, which is critical for enabling new AI models.

The award would create a robust leading-edge Dynamic Random-Access Memory (DRAM) ecosystem in the United States by investing in Micron's projects:

- **Clay, New York:** Construct first two high-volume manufacturing (HVM) fabs of planned four-fab "megafab" focused on leading-edge DRAM chip production. Each fab will have 600,000 square feet of cleanrooms, totaling 2.4 million square feet of cleanroom space across the four facilities – the largest amount of cleanroom space ever announced in the United States and the size of nearly 40 football fields.
- **Boise, Idaho:** Develop two high-volume manufacturing (HVM) fabs, each with approximately 600,000 square feet of cleanroom space focused on the production of leading-edge DRAM chips.

Leading-edge memory chips are foundational to all advanced technologies and Micron is the only U.S.-based manufacturer of memory chips. Currently, all leading-edge DRAM chip manufacturing takes place in East Asia, and Micron is widely recognized as a global leader in leading-edge DRAM technology and production. By supporting Micron's U.S. manufacturing plans, the Department is onshoring the development and production of the most advanced memory semiconductor technology which is crucial to advancing U.S. AI technology leadership and protecting our economic and national security.

As a result of this award, the development of the HVM fabs in Boise, Idaho, will be co-located with the company's R&D facility to improve efficiency across their R&D and manufacturing operations, reducing lags in technology transfer and cutting time-to-market for leading-edge memory products.

## Financial and Commercial Terms

As stated in the [CHIPS Notice of Funding Opportunity for Commercial Fabrication Facilities](https://www.nist.gov/document/notice-funding-opportunity-chips-incentives-program-commercial-fabrication-facilities) (<https://www.nist.gov/document/notice-funding-opportunity-chips-incentives-program-commercial-fabrication-facilities>), CHIPS for America will disburse direct funding to Micron for capital expenditures for the project based on the completion of

construction, production, and commercial milestones for amounts invested in capital expenditures. The CHIPS Program Office will track the performance of each CHIPS Incentives Award via financial and programmatic reports, in accordance with the award terms and conditions. All CHIPS funding recipients are required to adhere to restrictions pursuant to the CHIPS Act and NOFO regarding stock buybacks, National Security Guardrails, and sharing of intellectual property. The Department and Micron have agreed to certain conditions on buybacks for a period of 5 years.

<b>Micron Project Overview</b>		
<b>Project Summary</b>	<b>Recipient(s)</b>	Micron New York Semiconductor Manufacturing, LLC  Micron Idaho Semiconductor Manufacturing (TRITON), LLC  Micron Virginia Semiconductor Manufacturing, LLC
	<b>Location(s)</b>	Clay, New York  Boise, Idaho  Manassas, Virginia
<b>Financial Summary</b>	<b>Program</b>	CHIPS Program Office
	<b>Direct Funding Amount</b>	\$6.44 billion
	<b>Expected Capital Expenditure</b>	Up to \$150 billion over the next two decades
<b>Workforce Summary</b>	<b>Estimated Job Creation</b>	13,000 manufacturing and facility jobs* 10,500 construction jobs*  <i>*Over a 20-year time period</i>
	<b>CHIPS Workforce Funding</b>	\$65 million

<b>Project Statistics: Clay, New York</b>		
<b>Summary</b>	<b>Project Type</b>	Construction of first two high-volume leading-edge fabs of planned four-fab “megafab” focused on leading-edge DRAM chip production
	<b>Technology</b>	Leading edge DRAM chips
	<b>Estimated Job Creation</b>	9,000 manufacturing and facility jobs* 4,500 construction jobs* <i>*Over a 20-year time period</i>

**Related Links**

- [CHIPS for America Award Page: Micron's Boise, Idaho Project](https://www.nist.gov/chips/micron-idaho-boise)  
(<https://www.nist.gov/chips/micron-idaho-boise>)
- [CHIPS for America Award Page: Micron's Manassas, Virginia Project](https://www.nist.gov/chips/micron-virginia-manassas)  
(<https://www.nist.gov/chips/micron-virginia-manassas>)
- [Press Release: President Trump Secures \\$200B Investment from Micron Technology for Memory Chip Manufacturing in the United States](https://www.nist.gov/news-events/news/2025/06/president-trump-secures-200b-investment-micron-technology-memory-chip)  
(<https://www.nist.gov/news-events/news/2025/06/president-trump-secures-200b-investment-micron-technology-memory-chip>)
- [Fact Sheet: CHIPS Program Office Approach to Applicant Capital Allocation Policies](https://www.nist.gov/document/approach-capital-allocation-policies) (<https://www.nist.gov/document/approach-capital-allocation-policies>)

**Progress Photos**





Credit: Micron

**Application Stage** Final Award

**CHIPS Organization** CHIPS Program Office

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