U.S. Patent No. 8,327,051		
1[pre]: A portable handheld memory card comprising:	9[pre]: A method comprising: [a]: with a portable handheld card comprising a Universal Serial Bus (USB) port comprising a first set of pins; USB controller circuitry electrically connected with the first set of pins of the USB port; an input/output (I/O) port comprising a second set of pins; I/O controller circuitry electrically connected with the second set of pins of the I/O port; a memory in communication with the USB port and the I/O port; and a housing storing the memory and exposing the USB port and the I/O port, wherein the USB port and the I/O port are positioned to allow a same card-insertion direction irrespective of whether a host device comprises a mating USB port or a mating I/O port and wherein the USB port and the I/O port are positioned such that when the I/O port is electrically connected with the host device, at least one of the first set of pins of the USB port is not electrically connected to the host device, and when the USB port is electrically connected to the host device, at least one of the second set of pins of the I/O port is not electrically connected to the host device:	16[pre]: A method comprising: 16[a]: with a portable handheld card comprising a Universal Serial Bus (USB) port comprising a first set of pins; USB controller circuitry electrically connected with the first set of pins of the USB port; an input/output (I/O) port comprising a second set of pins; I/O controller circuitry electrically connected with the second set of pins of the I/O port; a memory in communication with the USB port and the I/O port; and a housing storing the memory and exposing the USB port and the I/O port, wherein the USB port and the I/O port are positioned to allow a same card-insertion direction irrespective of whether a host device comprises a mating USB port or a mating I/O port and wherein the USB port and the I/O port are positioned such that when the I/O port is electrically connected with the host device, at least one of the first set of pins of the USB port is not electrically connected to the host device, and when the USB port is electrically connected to the host device, at least one of the second set of pins of the I/O port is not electrically connected to the host device:
1[a]: a Universal Serial Bus (USB) port comprising a first set of pins;		
1[b]: USB controller circuitry electrically connected with the first set of pins of the USB port; and		
1[c]: an input/output (I/O) port comprising a second set of pins; and		
1[d]: I/O controller circuitry electrically connected with the second set of pins of the I/O port;		
1[e]: a memory in communication with the USB port and the I/O port; and		
1[f]: a housing storing the memory and exposing the USB port and the I/O port;		
1[g]: wherein the USB port and the I/O port are positioned on a same end to allow a same card-insertion direction irrespective of whether a host device comprises a mating USB port or a mating I/O port; and		
1[h]: wherein the USB port and the I/O port are positioned such that when the I/O port is electrically connected with the host device, at least one of the first set of pins of the USB port is not electrically connected to the host device, and		
1[i]: when the USB port is electrically connected to the host device, at least		

one of the second set of pins of the I/O port is not electrically connected to the host device.		
	9[b]: reading compressed data from the memory;	
	9[c]: decompressing the compressed data to decompressed data;	
	9[d]: transmitting the decompressed data on the I/O port;	
	9[e]: converting the compressed data to first converted data for transmission on the USB port;	
	9[f]: transmitting the first converted on the USB port;	
	12[pre]: The method of claim 9, further comprising:	
	12[a]: converting the decompressed data to second converted data for transmission on the I/O port; and	
	12[b]: transmitting the second converted data on the I/O port.	
		16[b]: reading data from the memory;
		16[c]: determining whether the data is to be transmitted via the USB port or I/O port; and
		16[d]: transmitting the data to the host device via the determined port.
4: The portable handheld memory card of claim 1 further comprising: a power management unit in communication with the USB controller circuitry.		

5: The portable handheld memory card of claim 1 further comprising: a host interface module in communication with the I/O port.		
6: The portable handheld memory card of claim 1 further comprising: circuitry configured to control read and write operations to the memory.	13: The method of claim 9, further comprising: controlling reading and writing to the memory.	
7: The portable handheld memory card of claim 1, wherein the I/O port comprises a Secure Digital port.	14: The method of claim 9, wherein the I/O port comprises a Secure Digital port.	20: The method of claim 16, wherein the I/O port comprises a Secure Digital port.
8: The portable handheld memory card of claim 1, wherein the memory comprises Flash memory.	15: The method of claim 9, wherein the memory comprises Flash memory.	21: The method of claim 16, wherein the memory comprises Flash memory.
23: The portable handheld memory card of claim 1, wherein at least two pins of the USB port are parallel to at least two pins of the I/O port.		
24: The portable handheld memory card of claim 1, wherein layout for the USB port is different from layout of the I/O port.		
25: The portable handheld memory card of claim 1, wherein the I/O port is configured for mating with an external port.		
26: The portable handheld memory card of claim 1, wherein all of the first set of pins electrically connected with the USB controller circuitry is not electrically connected to the I/O controller circuitry.		

27[pre]: The portable handheld memory card of claim 1,		
27[a]: wherein the USB port comprises multiple data lines,		
27[b]: wherein the first set of pins comprise multiple data pins connected to the multiple data lines; and		
27[c]: wherein all of the multiple data pins are not electrically connected to the I/O controller circuitry.		