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X86 vs. ARM: A Deep Dive into the Architecture

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In the vast world of the [semiconductor industry](#), the competition between X86 and ARM architectures has always been a focal point in the field. These two architectures represent the design philosophies of Complex Instruction Set Computing (CISC) and Reduced Instruction Set Computing (RISC) respectively, and have achieved significant accomplishments in different domains. This article will delve into the current standoff between X86 and ARM architectures, analyzing their respective advantages, market layouts, and future trends.

I. The Current Situation and Advantages of X86 Architecture

Since Intel released the 8086 processor in 1978, the X86 architecture has become one of the most widely used instruction set architectures in the computing field. Its main features include a complex instruction set, [high programmability](#), powerful processing capabilities, backward compatibility, and closed-source code. The X86 architecture is widely used in personal computers and servers, supporting a wide range of software and operating systems, and boasts a robust ecosystem and backward compatibility.

In terms of market share, the X86 architecture has long dominated the desktop PC, workstation, and data center server markets. According to Mercury Research, an CPU market tracking firm, in the second quarter of 2024, Intel held a 78.9% market share in the PC market, while [AMD](#) accounted for 21.1%. In the server market, according to IDC data, x86 servers accounted for 88% of the market share in 2023, with China's x86 server shipments reaching 3.62 million units, and a projected growth of 5.7% in 2024.

The advantage of the X86 architecture lies in its rich instruction set, which provides comprehensive functional support for complex operating systems and applications, and excels in high-performance computing. For example, [AMD's Epyc series processors](#) and [Intel's Xeon series processors](#) demonstrate strong performance in the data center segment.

II. Current Situation and Advantages of ARM Architecture

ARM architecture is a low-power RISC architecture widely used in mobile devices and embedded systems. Its main features include low power consumption, high performance, scalability, wide application, and open-source support. ARM architecture holds an absolute dominance in the smartphone and tablet markets, with a market share of over 90% globally for mobile devices using [ARM-based processors](#).

In recent years, ARM architecture has gradually emerged in the PC market, posing a certain impact on the traditional dominant areas of X86. According to relevant data, as of 2024, X86 still holds a higher market share in the PC market, but ARM-based chips have captured 8% of the PC market share and continue to grow. Apple's M-series chips excel in performance and energy efficiency, setting a benchmark for ARM's development in the PC market. Meanwhile, Qualcomm, MediaTek, and other vendors have also launched ARM-based processors for PCs, supported by Microsoft, ASUS, Acer, and other PC vendors.

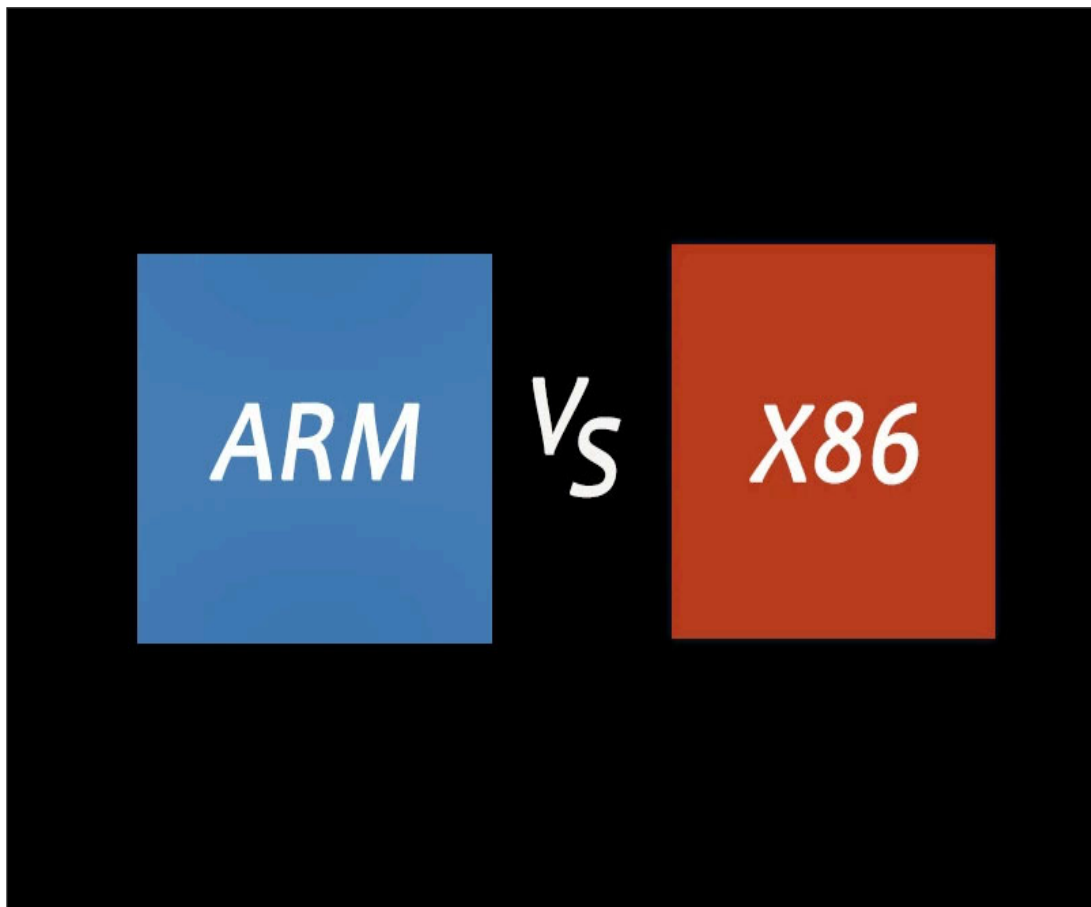
In the server market, some cloud service providers have begun adopting servers based on ARM architecture to reduce energy consumption and operating costs. For example, Qualcomm's ARM-based [PC chip](#), the X Elite, demonstrates outstanding performance and has received support from multiple OEM vendors.

III. Competition and Cooperation Between X86 and ARM

The competition between X86 and ARM architectures mainly focuses on their respective market layouts and ecosystems. The X86 architecture, with its advantages in high-performance computing and servers, has long dominated the market. In contrast, ARM architecture, with its low power consumption and high [energy efficiency](#), holds an absolute advantage in the mobile device market and gradually infiltrates the PC and server markets.

However, as technology continues to evolve, the boundaries between X86 and ARM architectures are gradually blurring. For example, Intel has launched Core Ultra processors with integrated AI-specific NPUs, aiming to capture a share of the AI PC market. Meanwhile, ARM-based processors are continuously improving in performance and energy efficiency to meet the demands of the PC and server markets.

Furthermore, cooperation between X86 and ARM architectures is also emerging. For instance, AMD and Intel have jointly initiated the "x86 Advisory Group" to collaboratively improve the x86 Instruction Set Architecture (ISA), promoting unity within the x86 camp, enhancing compatibility, and simplifying software development. Additionally, Microsoft's Windows operating system is deepening its adaptation to native ARM, expanding ARM's application in the PC market.



IV. Future Trends

In the future, competition between X86 and ARM architectures will intensify. With the continuous development of AI technology, AI PCs will become a new hotspot in the market. Both X86 and ARM architectures are actively positioning themselves in the AI PC market, aiming to capture market share by providing high-performance and low-power solutions.

Simultaneously, with the continuous development of cloud computing and big data technologies, the server market has increasingly stringent requirements for energy efficiency and performance. ARM architecture, with its low power consumption and high energy efficiency, has immense potential in the server market. In contrast, the X86 architecture needs to continuously improve its performance and energy efficiency to address the challenges posed by ARM architecture.

Moreover, as semiconductor technology continues to advance, the cost and difficulty of [chip manufacturing](#) are also increasing. Cooperation between X86 and ARM architectures will become a future trend. Through collaboration, both parties can share technology and resources, reducing manufacturing costs and enhancing market competitiveness.

V. Conclusion

The competition and cooperation between X86 and ARM architectures are currently focal points in the chip industry. The X86 architecture dominates in high-performance computing and servers, while ARM architecture holds an absolute advantage in the mobile device market. In the future, as technology continues to evolve and markets change, competition between X86 and ARM architectures will intensify, but there will also be more opportunities for cooperation. Both parties need to continuously enhance their technical strength and market competitiveness to address future challenges and opportunities.

PC Computer chips, Purchased at [Semicone Store](#)

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- The [OP249GSZ](#) is a high-speed, precision dual JFET [operational amplifier](#) with a typical slew rate of 22 V/μs and a maximum settling time of 1.2 μs to 0.01%, making it suitable for high-speed bipolar DAC and ADC applications, as well as professional audio and other demanding signal processing tasks.
- The [88E3018-A2-NNC11000](#) is a high-performance Ethernet PHY chip designed for use in Gigabit Ethernet applications, featuring robust performance and advanced energy management capabilities.

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