

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY LTD.,  
Petitioner,

v.

ADVANCED INTEGRATED CIRCUIT PROCESS LLC,  
Patent Owner.

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Case IPR2025-00683  
Patent No. 8,907,425

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**PETITIONER'S UPDATED EXHIBIT LIST**

Pursuant to 37 C.F.R. § 42.63(e), Petitioner Taiwan Semiconductor Manufacturing Company Ltd. hereby submits a current listing of Petitioner exhibits. Exhibits 1151-1155 are being filed today.

<b>Exhibit</b>	<b>Description</b>
1001	U.S. Patent No. 8,907,425 B2 to Itou et al. (“the ’425 Patent”).
1002	Prosecution History of the ’425 Patent (excerpted) (“File History”).
1003	Declaration of Scott E. Thompson, Ph.D.
1004	Curriculum Vitae of Scott E. Thompson, Ph.D.
1005	U.S. Pat. Pub. No. 2009/0246922 A1 to Wu et al. (“Wu”).
1006	U.S. Pat. Pub. No. 2007/0249069 A1 to Alvarez et al. (“Alvarez”).
1007	S. Wolf, <i>Silicon Processing for the VLSI Era: Volume 4—Deep-Submicron Process Technology</i> (2002) (excerpted) (“Wolf”).
1008	U.S. Pat. Pub. No. 2005/0112817 A1 to Cheng et al. (“Cheng”).
1009	U.S. Pat. Pub. No. 2007/0034906 A1 to Wang et al. (“Wang”).
1010	U.S. Pat. Pub. No. 2008/0029825 A1 to Saito et al. (“Saito”).
1011	U.S. Pat. Pub. No. 2005/0285203 A1 to Fukutome et al. (“Fukutome”).
1012	D. James, “2004 – The Year of 90-nm: A Review of 90 nm Devices,” 2005 IEEE/SEMI Advanced Semiconductor Manufacturing Conference (2005) (“James”).
1013	S.M. Sze, <i>Physics of Semiconductor Devices</i> (2d ed. 1981) (excerpted).
1014	J.D. Plummer et al., <i>Silicon VLSI Technology: Fundamentals, Practice and Modeling</i> (2000) (excerpted).

Exhibit	Description
1015	S.E. Thompson et al., “A 90-nm Logic Technology Featuring Strained Silicon,” IEEE Transactions on Electron Devices, vol. 51. No. 11, pp. 1790-97 (Nov. 2004).
1016	Y. Sun et al., “Physics of Strain Effects in Semiconductors and Metal-Oxide-Semiconductor Field-Effect Transistors,” Journal of Applied Physics, vol. 101, Art. No. 104503 (22 pages) (May 2007).
1017	T. Ghani et al., “A 90nm High Volume Manufacturing Logic Technology Featuring Novel 45nm Gate Length Strained Silicon CMOS Transistors,” Technical Digest of the 2003 IEEE International Electron Devices Meeting (“IEDM”), pp. 978-80 (Dec. 10, 2003).
1018	U.S. Pat. Pub. No. 2004/0262683 A1 to Bohr et al.
1019	U.S. Pat. Pub. No. 2006/0286729 A1 to Kavalieros et al.
1020	U.S. Pat. Pub. No. 2006/0148151 A1 to Murthy et al.
1021	U.S. Pat. Pub. No. 2011/0042729 A1 to Chen et al.
1022	P. Morin et al., “Extensive Study of the Correlation between ContactEtch Stop Nitride Material Properties and Negative Bias Temperature Instabilities Measured in pMOSFETS,” ECS Transactions, vol. 6, no. 3, pp. 355-69 (2007).
1023	U.S. Pat. Pub. No. 2007/0235823 A1 to Hsu et al.
1024	U.S. Pat. Pub. No. 2005/0170104 A1 to Jung et al.
1025	U.S. Pat. Pub. No. 2008/0145984 A1 to Ke et al.
1026	P. Bai et al., “A 65nm Logic Technology Featuring 35nm Gate Lengths, Enhanced Channel Strain, 8 Cu Interconnect Layers, Low-k ILD and 0.57 $\mu\text{m}^2$ SRAM Cell,” Technical Digest of the 2004 IEEE International Electron Devices Meeting (IEDM), pp. 657-60 (Dec. 2004).

Exhibit	Description
1027	K. Mistry et al., “A 45nm Logic Technology with High-k+Metal Gate Transistors, Strained Silicon, 9 Cu Interconnect Layers, 193nm Dry Patterning, and 100% Pb-Free Packaging,” Technical Digest of the of the 2007 IEEE International Electron Devices Meeting (IEDM), pp. 247-50 (Dec. 2007).
1028	S. Natarajan et al., “A 32nm Logic Technology Featuring 2 <sup>nd</sup> -Generation High-k + Metal-Gate Transistors, Enhanced Channel Strain and 0.171 $\mu\text{m}^2$ SRAM Cell Size in a 291Mb Array,” Technical Digest of the of the 2008 IEEE International Electron Devices Meeting (IEDM), pp. 1-3 (Dec. 2008).
1029	U.S. Pat. Pub. No. 2004/0262784 A1 to Doris et al.
1030	U.S. Pat. No. 6,797,556 B2 to Murthy et al.
1031	U.S. Pat. Pub. No. 2007/0134870 A1 to Lee et al.
1032	U.S. Pat. Pub. No. 2005/0260810 A1 to Cheng et al.
1033	U.S. Pat. Pub. No. 2009/0020820 A1 to Baik et al.
1034	U.S. Pat. Pub. No. 2008/0293207 A1 to Koutny et al.
1035	U.S. Pat. Pub. No. 2010/0075476 A1 to Miyashita.
1036	Declaration of Shauna Wiest in support of public availability of James (2004).
1037	Declaration of Lauren Gluckman in support of public availability of Wolf (2002).
1038	Declaration of Rachel Watters in support of public availability of Wolf (2002).
1039	S. Thompson et al., “Source/Drain Extension Scaling for 0.1 $\mu\text{m}$ and Below Channel Length MOSFETS,” Digest of Technical Papers for the 1998 Symposium on VLSI Technology, pp. 132-33 (1998).

Exhibit	Description
1040	D.M. Fleetwood et al., <i>Defects in Microelectronic Materials and Devices</i> (2008).
1041	U.S. Pat. Pub. No. 2006/0246641 A1 to Kammler et al.
1042	U.S. Pat. Pub. No. 2007/0228482 A1 to Wei et al.
1043	U.S. Pat. Pub. No. 2008/0203486 A1 to Wiatr et al.
1044	U.S. Pat. Pub. No. 2009/0242995 A1 to Suzuki et al.
1045	U.S. Pat. Pub. No. 2007/0090465 A1 to Suzuki et al.
1046	A. Pavlov & M. Sachdev, <i>CMOS SRAM Circuit Design and Parametric Test in Nano-Scaled Technologies: Process-Aware SRAM Design and Test</i> (2008) (excerpted).
1047	U.S. Pat. Pub. No. 2007/0023832 A1 to Matsui.
1048	M. Ishida et al., “A Novel 6T-SRAM Cell Technology Designed with Rectangular Patterns Scalable Beyond 0.18 $\mu\text{m}$ Generation and Desirable for Ultra High Speed Operation,” Proceedings of the 1998 International Electron Devices Meeting (IEDM), pp. 201-04 (1998).
1049	D. Balobas & N. Konofaos, “Design and Evaluation of 6T SRAM Layout Designs at Modern Nanoscale CMOS Processes,” 4th International Conference on Modern Circuits and System Technologies (2015).
1050	U.S. Pat. Pub. No. 2011/0074498 A1 to Thompson et al.
1051	Second Amended Docket Control Order, <i>Advanced Integrated Circuit Process LLC v. United Microelectronics Corporation et al.</i> , No. 2:24-cv-00730-JRG (E.D. Tex.).
1052	Reserved.

Exhibit	Description
1053	Report of Judge Rodney Gilstrap, Calendar of Events Set for 6/1/2026/-6/30/2026 (PACER) (last visited June 27, 2025), <i>available at</i> <a href="https://ecf.txed.uscourts.gov/cgi-bin/CalEvents.pl?143870417754058-L_1_0-1">https://ecf.txed.uscourts.gov/cgi-bin/CalEvents.pl?143870417754058-L_1_0-1</a> .
1054	Andrew T. Dufresne et al., How reliable are trial dates relied on by the PTAB in the Fintiv analysis? (Oct. 29, 2021).
1055	United States District Courts – National Judicial Caseload Profile, 12-Month Period Ending Mar. 31, 2025, U.S. Courts (2025), <i>available at</i> <a href="https://www.uscourts.gov/sites/default/files/document/fcms_na_dist_profile0331.2025.pdf">https://www.uscourts.gov/sites/default/files/document/fcms_na_dist_profile0331.2025.pdf</a> .
1056	Summary of Judge Gilstrap Patent Trials Within One-Year Period of July 12, 2024, to July 11, 2025.
1057	Summary of cases by year for Judge Gilstrap, Docket Navigator, <i>available at</i> <a href="https://search/docketnavigator.com/patent/judge/14373/0?print=true&amp;e=false">https://search/docketnavigator.com/patent/judge/14373/0?print=true&amp;e=false</a> .
1058	Summary of Post-Trial Briefing Timelines for Judge Gilstrap Trials Since 2023, based on LegalMetric data.
1059	Reserved.
1060	Letter from Sen. Thom Tillis to Acting Director Andrew Hirshfeld (Nov. 2, 2021).
1061	Letter from Sens. Thom Tillis and Patrick Leahy to Hon. Chief Justice John Roberts (Nov. 2, 2021).
1062	2021 Year-End Report on the Federal Judiciary, Supreme Court of the United States (Dec. 31, 2025) (last visited July 15, 2025), <i>available at</i> <a href="https://www.supremecourt.gov/publicinfo/year-end/2021year-endreport.pdf">https://www.supremecourt.gov/publicinfo/year-end/2021year-endreport.pdf</a> .

Exhibit	Description
1063	<i>Resonant Sys., Inc. v. Samsung Elecs. Co. Ltd.</i> , No. 2:22-cv-00423-JRG, Dkt. 41, Second Amended Docket Control Order (E.D. Tex. Apr. 24, 2023).
1064	<i>Resonant Sys., Inc. v. Samsung Elecs. Co. Ltd.</i> , No. 2:22-cv-00423-JRG, Dkt. 58, Third Amended Docket Control Order (E.D. Tex. Nov. 21, 2023).
1065	<i>Resonant Sys., Inc. v. Samsung Elecs. Co. Ltd.</i> , No. 2:22-cv-00423-JRG, Docket (E.D. Tex.).
1066	<i>Commc 'n Techs. Inc., v. Samsung Elecs. Co., Ltd.</i> , No. 2:21-cv-00444-JRG, Dkt. 34, Docket Control Order (E.D. Tex. Apr. 6, 2022).
1067	<i>Commc 'n Techs. Inc., v. Samsung Elecs. Co., Ltd.</i> , No. 2:21-cv-00444-JRG, Dkt. 83, Second Amended Docket Control Order (E.D. Tex. Oct. 31, 2022).
1068	<i>Commc 'n Techs. Inc., v. Samsung Elecs. Co., Ltd.</i> , No. 2:21-cv-00444-JRG, Dkt. 133, Third Amended Docket Control Order (E.D. Tex. Feb. 2, 2023).
1069	<i>Commc 'n Techs. Inc., v. Samsung Elecs. Co., Ltd.</i> , No. 2:21-cv-00444-JRG, Docket (E.D. Tex.).
1070	<i>Pictiva Displays Int'l Ltd. v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00495-JRG, Dkt. 40, Docket Control Order (E.D. Tex. Apr. 17, 2024).
1071	<i>Pictiva Displays Int'l Ltd. v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00495-JRG, Dkt. 224, First Amended Docket Control Order (E.D. Tex. Apr. 21, 2025).
1072	<i>Pictiva Displays Int'l Ltd. v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00495-JRG, Docket (E.D. Tex.).
1073	<i>Cyandia, Inc. v. SAP Am., Inc.</i> , No. 2:24-cv-00096-JRG, Dkt. 32, Docket Control Order (E.D. Tex. June 17, 2024).

Exhibit	Description
1074	<i>Cyandia, Inc. v. SAP Am., Inc.</i> , No. 2:24-cv-00096-JRG, Dkt. 102, First Amended Docket Control Order (E.D. Tex. May 9, 2025).
1075	<i>Cyandia, Inc. v. SAP Am., Inc.</i> , No. 2:24-cv-00096-JRG, Docket (E.D. Tex.).
1076	<i>Emerging Auto. LLC v. Kia Corp.</i> , No. 2:23-cv-0437-JRG, Dkt. 40, Docket Control Order (E.D. Tex. February 6, 2024).
1077	<i>Emerging Auto. LLC v. Kia Corp.</i> , No. 2:23-cv-0437-JRG, Dkt. 265, First Amended Docket Control Order (E.D. Tex. May 21, 2025).
1078	<i>Emerging Auto. LLC v. Kia Corp.</i> , No. 2:23-cv-0437-JRG, Docket (E.D. Tex.).
1079	<i>Truesight Commc'ns LLC v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00643-JRG, Dkt. 31, Docket Control Order (E.D. Tex. June 24, 2024).
1080	<i>Truesight Commc'ns LLC v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00643-JRG, Dkt. 59, Second Amended Docket Control Order (E.D. Tex. Jan. 8, 2025).
1081	<i>Truesight Commc'ns LLC v. Samsung Elecs. Co. Ltd.</i> , No. 2:23-cv-00643-JRG, Dkt. 67, Third Amended Docket Control Order (E.D. Tex. Jan. 24, 2025).
1082	Declaration of Michael Shen.
1083	TSMC's stipulation regarding U.S. Patent Nos. 8,198,686; 8,907,425; 7,579,227; 7,923,764; 8,253,180; 8,587,076; and 8,796,779.
1084	United Microelectronics Corp. and UMC Group (USA)'s stipulations regarding U.S. Patent Nos. 8,587,076; 8,253,180; 7,579,227; 8,907,425; 8,198,686; 7,923,764; and 8,796,779.
1085	Reserved.

Exhibit	Description
1086	Institution Decisions for TSMC, based on Lex Machina data.
1087	Business Organizations Inquiry for Advanced Integrated Circuit Process LLC, Filing No. 805585855, Texas Secretary of State.
1088	Certificate of Formation Limited Liability Company for Advanced Integrated Circuit Process LLC, Filing No. 805585855, Texas Secretary of State (June 12, 2024).
1089	Patent Assignment Record from Nuvoton Technology Corp. Japan to Advanced Integrated Circuit Process LLC, Reel: 068118 Frame: 0314-0317, United States Patent and Trademark Office (July 30, 2024).
1090	<i>Intellectual Ventures I LLC v. T-Mobile USA, Inc.</i> , No. 2:17-cv-557-JRG, Dkt. 297, Pre-Trial Hearing Transcript (E.D. Tex. Jan. 3, 2019).
1091	<i>Advanced Integrated Circuit Process LLC v. United Microelectronics Corp.</i> , No. 2:24-cv-0730-JRG (Lead Case), AICP’s P.R. 3-1 Disclosure of Asserted Claims and Infringement Contentions and P.R. 3-2 Document Production Against TSMC (E.D. Tex. Jan. 7, 2025).
1092	Dockets in <i>Advanced Integrated Circuit Process LLC v. United Microelectronics Corp.</i> , No. 2:24-cv-0730-JRG (Lead Case), Docket (E.D. Tex.).
1093	Reserved.
1094	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01090, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00683 (P.T.A.B. June 30, 2025).

Exhibit	Description
1095	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01093, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00831 (P.T.A.B. June 30, 2025).
1096	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01092, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00830 (P.T.A.B. June 30, 2025).
1097	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01091, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00682 (P.T.A.B. June 30, 2025).
1098	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01079, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00829 (P.T.A.B. June 30, 2025).
1099	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01076, Paper 7, Motion for Joinder Under 35 U.S.C. § 315(c) and 37 C.F.R. § 42.122(b) to Related <i>Inter Partes</i> Review IPR2025-00828 (P.T.A.B. June 30, 2025).
1100	<i>United Microelectronics Corp. v. Advanced Integrated Circuit Process LLC</i> , IPR2025-01053, Paper 7, Motion for Consolidation Under 35 U.S.C. § 315(d) and 37 C.F.R. § 42.122(a) with Related <i>Inter Partes</i> Review IPR2025-00832 (P.T.A.B. June 30, 2025).
1101	Listing of district court cases and <i>inter partes</i> review proceedings in which U.S. Patent No. 8,907,425 has been asserted or challenged, Docket Navigator.
1102	Continuity data for U.S. Patent No. 8,907,425, Patent Center, United States Patent and Trademark Office.
1103	Reserved.

Exhibit	Description
1104	Reserved.
1105	Delaware Secretary of State Entity Search Status for AMTL LLC.
1106	Final Written Decision Outcomes for TSMC, <i>available at</i> <a href="https://law.lexmachina.com/party/ptab?filing_date-from=2012-09-16&amp;filing_date-to=&amp;final_decision_date-from=2013-07-17&amp;final_decision_date-to=&amp;new_trial_flow-exclude=PreI_AS&amp;party_role-include=Petitioner&amp;filters=true&amp;id=26931&amp;id=20676624&amp;id=64431208&amp;tab=ptab_trial_flow&amp;view=analytics&amp;cols=trial%2Cfiled%2Cinstitution_decision_date%2Cpatent%2Cpetitioners%2Cpatent_owners%2Ctrial_resolution%2Cfinal_decision_date">https://law.lexmachina.com/party/ptab?filing_date-from=2012-09-16&amp;filing_date-to=&amp;final_decision_date-from=2013-07-17&amp;final_decision_date-to=&amp;new_trial_flow-exclude=PreI_AS&amp;party_role-include=Petitioner&amp;filters=true&amp;id=26931&amp;id=20676624&amp;id=64431208&amp;tab=ptab_trial_flow&amp;view=analytics&amp;cols=trial%2Cfiled%2Cinstitution_decision_date%2Cpatent%2Cpetitioners%2Cpatent_owners%2Ctrial_resolution%2Cfinal_decision_date</a> .
1107-1109	Reserved.
1110	Number of IPR Final Written Decisions Finding at Least One Claim Unpatentable (2020-2025).
1111	Trump on China Putting - America First (Robert C. O'Brien editor).
1112	YiTan Hsu, The Critical Role of Taiwan in the United States Semiconductor Supply Chain (Nov. 24, 2024).
1113	Letter from Billie Chen to Under Secretary Andrei Iancu regarding USPTO Request for Comments on Discretion to Institute Trials Before the Patent Trial and Appeal Board, Docket No. PTO-C-2020-055.
1114	Reserved.
1115	U.S., Taiwan, and Semiconductors: A Critical Supply Chain Partnership, Final Report, Project 2049 Institute (US-Taiwan Business Counsel June 21, 2023).
1116	TSMC Adds High-K Metal Gate Low Power Process to 20nm Road Map, TSMC (Aug. 24, 2009) (last visited July 16, 2025), <i>available at</i> <a href="https://pr.tsmc.com/english/news/1572">https://pr.tsmc.com/english/news/1572</a> .

Exhibit	Description
1117	TSMC 28nm Technology in Volume Production, PR Newswire (Oct. 24, 2011) (last visited July 16, 2025), <i>available at</i> <a href="https://www.prnewswire.com/news-releases/tsmc-28nm-technology-in-volume-production-132425858.html">https://www.prnewswire.com/news-releases/tsmc-28nm-technology-in-volume-production-132425858.html</a> .
1118	Shannon Davis, New Structure Transistors for Advanced Technology Node CMOS ICs, Semiconductor Digest (Mar. 22, 2024) (last visited July 14, 2025), <i>available at</i> <a href="https://www.semiconductor-digest.com/new-structure-transistors-for-advanced-technology-node-cmos-ics/">https://www.semiconductor-digest.com/new-structure-transistors-for-advanced-technology-node-cmos-ics/</a> .
1119	Another Historic Investment Secured Under President Trump, Articles, The White House (Mar. 3, 2025) (last visited July 14, 2025), <i>available at</i> <a href="https://www.whitehouse.gov/articles/2025/03/another-historic-investment-secured-under-president-trump/">https://www.whitehouse.gov/articles/2025/03/another-historic-investment-secured-under-president-trump/</a> .
1120	About TSMC Washington, TSMC (last visited July 14, 2025), <i>available at</i> <a href="https://www.tsmc-washington.com/en/about/index.html">https://www.tsmc-washington.com/en/about/index.html</a> .
1121	Russ Wiles, US Commerce Secretary visits TSMC complex in Phoenix as work on third fab starts, azcentral. (Apr. 30, 2025, updated May 1, 2025) (last visited July 15, 2025), <i>available at</i> <a href="https://www.azcentral.com/story/money/business/tech/2025/04/30/u-s-commerce-secretary-howard-lutnick-visits-tsmc-complex-in-phoenix/83353830007/">https://www.azcentral.com/story/money/business/tech/2025/04/30/u-s-commerce-secretary-howard-lutnick-visits-tsmc-complex-in-phoenix/83353830007/</a> .
1122	Collaboration Programs, University Centers, TSMC (last visited July 14, 2025), <i>available at</i> <a href="https://research.tsmc.com/english/collaborations/academic/academic-programs.html">https://research.tsmc.com/english/collaborations/academic/academic-programs.html</a> .
1123	CNBC Transcript: United States Commerce Secretary Howard Lutnick Speaks with CNBC’s Brian Sullivan on “The Exchange” Today, CNBC (Apr. 29, 2025) (last visited July 14, 2025), <i>available at</i> <a href="https://www.cnbc.com/2025/04/29/cnbc-transcript-united-states-commerce-secretary-howard-lutnick-speaks-with-cnbc-brian-sullivan-on-the-exchange-today.html">https://www.cnbc.com/2025/04/29/cnbc-transcript-united-states-commerce-secretary-howard-lutnick-speaks-with-cnbc-brian-sullivan-on-the-exchange-today.html</a> .

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1124	Silvia Pellegrino, What is TSMC?, Tech Monitor (Jan. 2, 2023) (last visited July 14, 2025), <i>available at</i> <a href="https://www.techmonitor.ai/what-is/what-is-tsmc/?cf-view">https://www.techmonitor.ai/what-is/what-is-tsmc/?cf-view</a> .
1125	Lucas Tsai, TSMC: Enabling Startups to Unleash New Semiconductor Innovation, TSMC Blog (Apr. 18, 2022) (last visited July 14, 2025), <i>available at</i> <a href="https://www.tsmc.com/english/news-events/blog-article-20220418">https://www.tsmc.com/english/news-events/blog-article-20220418</a> .
1126	Silent Giant TSMC Returns to the Peak of its Powers as the AI Boom Continues, Yahoo! finance (June 12, 2025), <i>available at</i> <a href="https://finance.yahoo.com/news/silent-giant-tsmc-returns-peak-230300515.html?guccounter=1&amp;guce_referrer=aHR0cHM6Ly93d3cuZ29vZ2xlLmNvbS8&amp;guce_referrer_sig=AQAAAGbNeBi6fbz4U1LAPZACoSOpXFKhZ2d8Aeym1OTBUscShGSwm5cDQL_KDbidYk16TkPx7YtIzsB2YbOGeGdO4PchwSBFYrk3fTg3HWr-MVsPIL1q7mWa7ZR5e6BG0tbKASjEzPE-8r-aiT0KakQ2cXTjpdVs4xTGUvCoXUGIVbDT">https://finance.yahoo.com/news/silent-giant-tsmc-returns-peak-230300515.html?guccounter=1&amp;guce_referrer=aHR0cHM6Ly93d3cuZ29vZ2xlLmNvbS8&amp;guce_referrer_sig=AQAAAGbNeBi6fbz4U1LAPZACoSOpXFKhZ2d8Aeym1OTBUscShGSwm5cDQL_KDbidYk16TkPx7YtIzsB2YbOGeGdO4PchwSBFYrk3fTg3HWr-MVsPIL1q7mWa7ZR5e6BG0tbKASjEzPE-8r-aiT0KakQ2cXTjpdVs4xTGUvCoXUGIVbDT</a> .
1127	Reserved.
1128	Reserved.
1129	Removing Barriers to American Leadership in Artificial Intelligence, Presidential Actions, The White House (Jan. 23, 2025) (last visited July 14, 2025), <i>available at</i> <a href="https://www.whitehouse.gov/presidential-actions/2025/01/removing-barriers-to-american-leadership-in-artificial-intelligence/">https://www.whitehouse.gov/presidential-actions/2025/01/removing-barriers-to-american-leadership-in-artificial-intelligence/</a> .
1130	TSMC Expands Collaboration with Ansys by Integrating AI Technology to Accelerate 3D-IC Design, Ansys (Sept. 25, 2024) (last visited July 14, 2025), <i>available at</i> <a href="https://investors.ansys.com/news-releases/news-release-details/tsmc-expands-collaboration-ansys-integrating-ai-technology">https://investors.ansys.com/news-releases/news-release-details/tsmc-expands-collaboration-ansys-integrating-ai-technology</a> .

Exhibit	Description
1131	Cadence and TSMC Advance AI and 3D-IC Chip Design with Certified Design Solutions for TSMC’s A16 and N2P Process Technologies, Cadence (Apr. 23, 2025) (last visited July 14, 2025), <i>available at</i> <a href="https://www.cadence.com/en_US/home/company/newsroom/press-releases/pr/2025/cadence-and-tsmc-advance-ai-and-3d-ic-chip-design-with-certified.html">https://www.cadence.com/en_US/home/company/newsroom/press-releases/pr/2025/cadence-and-tsmc-advance-ai-and-3d-ic-chip-design-with-certified.html</a> .
1132	Reserved.
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Exhibit	Description
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1144	<p>Allowance Rate for U.S. patent applications for TSMC, from <a href="https://app.juristat.com/dashboard/premium-benchmarks">https://app.juristat.com/dashboard/premium-benchmarks</a>.</p>
1145	<p>Excerpted data on U.S. utility patent applications from ifi Claims Patent Services December 2024 Monthly Report – U.S. Utility Grants and Applications (Dec. 2024).</p>

Exhibit	Description
1146	ifi Claims Patent Services, Top 50 patent grants from 2022-2024, <i>available at</i> <a href="https://www.ificlaims.com/rankings/top-50-2024/">https://www.ificlaims.com/rankings/top-50-2024/</a> , <a href="https://www.ificlaims.com/rankings/top-50-2023/">https://www.ificlaims.com/rankings/top-50-2023/</a> , <a href="https://www.ificlaims.com/rankings/top-50-2022/">https://www.ificlaims.com/rankings/top-50-2022/</a> .
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1154	Y. Shuhei, "China's Semiconductor Industry Avoids 'Blockage Against Miniaturization,'" Discuss Japan-Japan Foreign Policy Forum, No. 86 (Apr. 25, 2025), <i>available at</i> <a href="https://www.japanpolicyforum.jp/pdf/2025/no86/DJweb_86_eco_03.pdf">https://www.japanpolicyforum.jp/pdf/2025/no86/DJweb_86_eco_03.pdf</a> .
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Respectfully submitted,

Dated: July 31, 2025

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**CERTIFICATE OF SERVICE**

The undersigned hereby certifies that a copy of the foregoing **Petitioner's Updated Exhibit List** and **Exhibits 1151-1155** were served on July 31, 2025, via e-mail directed to counsel of record for the Patent Owner at the following:

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